

Date Application Submitted: 18-Dec-2025

Submitter Information

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Dr	Panagiota		Nikolaou
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Phone:

About the Event

Conference Record Number:	
70418	
Conference Title:	
2026 IFIP/IEEE 34rd International Conference on Very Large Scale Integration (VLSI-SoC)	
Conference Acronym:	
VLSI-SoC	
Event Format:	
In-person	
Event Type:	
Conference	
Explain if Other:	
Event registration system	
Conference Management Company Registration Service	
Conference Management Company Registration Service	
EasyConferences, Cyprus	
Conference Website URL:	
www.vlsi-soc.com	
Conference Webmaster Contact First Name:	
Florenc	
Conference Webmaster Contact Last Name:	
Demrozi	
Conference Webmaster Contact Email:	
florenc.demrozi@uis.no	
Start Date:	End Date:
2026-10-11	2026-10-14
Keywords:	
Applications and Architectures for AI and ML, Communication Architectures and	

Technologies, Computing Paradigms, Digital Circuits Design and Synthesis, Embedded System Design, Emerging Technologies and Applications, Low-Power, Energy-Efficient and Thermal-Aware Design, Security, Sensing and Signal Processing Technologies, Smart Systems, Devices and Applications, System Specification, Simulation and Verification, Test and Dependability,		
Scope:		
The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems.		
Field of Interest:		
Components, Circuits, Devices and Systems Computing and Processing Signal Processing and Analysis Power, Energy and Industry Applications Robotics and Control Systems		
Estimated Attendance:		
120		
Will there be exhibits?		
N		
Number of Exhibits:		
Will there be tutorials?		
Y		
Country:	State/Province:	City:
Cyprus		Limassol

Membership / Student Grants

Would you like to promote IEEE Membership at your event?
Y
Would you like to promote IEEE Society Membership at your event?
Y
Does your society or event provide grant(s) for student members to attend your technical conference and/or student competition?
Y
Please provide the url which contains the details on your student support:
not yet available

About the Location

Venue

Venue Name:
St. Raphael Resort
Address 1:
Amathountos Avenue 502, Pyrgos
Address 2:
Address 3:
Address 4:

Country:	State/Province:	City:
CY		Limassol
Zip/Postal Code:		
4520		

Sponsors

Sponsor: 1
IEEE Circuits and Systems Society (25.0)

Sponsor: 2
IEEE Council on Electronic Design Automation (25.0)

Sponsor: 3
University of Limerick (25.0)
Non-IEEE Signatory Name:
Tiziana Margaria
Non-IEEE Signatory Email Address:
tiziana.margaria@ul.ie

Sponsor: 4
IFIP WG10.5 (0.0)
Non-IEEE Signatory Name:
Graziano Pravadelli
Non-IEEE Signatory Email Address:
graziano.pravadelli@univr.it

Sponsor: 5
Ecole Centrale de Lyon (25.0)
Non-IEEE Signatory Name:
Ian O'Connor
Non-IEEE Signatory Email Address:
ian.oconnor@ec-lyon.fr

Total Financial Share:
100%
Estimated Revenue:
72000 EUR

Estimated Expense:
71000 EUR
Will this conference use IEEE Concentration Banking:
N
Who will own the Conference Name?
0
Other Text
IFIP WG 10.5

Conference Committee Responsibilities

Will this conference produce a conference publication?
Y
Will IEEE hold the copyright?
Y
Who will hold the copyright?
Will you request publication of your conference proceedings in IEEE Xplore?
Y

Describe how each IEEE sponsor, or their representative, is directly and substantially involved in developing the Technical Program.
IEEE members belonging to both CAS and CEDA will be involved in the technical program committee as chairs and members of some of the topic tracks. They will be invited to propose special sessions and keynotes too.
List the sponsors that are represented on the Technical Program committee
IEEE CAS, IEEE CEDA, IFIP WG10.5, University of Limerick, Ecole Centrale de Lyon, University of Stavanger, University of Central Lancashire Cyprus
Describe the role of each sponsor represented on the Technical Program Committee
VLSI-SOC is the flagship conference of IFIP WG10.5, whose members take care of steering the conference themes year by year. University of Limerick, Ecole Centrale de Lyon, University of Stavanger and University of Central Lancashire Cyprus are in charge of organizing the conference venue for 2026. IEEE CAS and IEEE CEDA are widely represented in the conference technical program committee; their members will take part in the decision concerning topic tracks and paper selections.
Does the conference committee have full authority to operate the conference? Yes/No
No
Is the oversight committee made up of representatives from each sponsor?
Yes. The oversight committee is composed of representatives from each sponsoring organization. Each sponsor designates one or more members who participate in overseeing the planning, execution, and compliance aspects of the conference, ensuring balanced representation and coordinated decision-making.
Who appoints the conference committee chairs and other key members?
The general chairs are selected by the VLSI-SOC steering committee and the IFIP WG10.5 after

the presentation of a candidature composed of venue, preliminary budget, proposal for the organizing committee. After the approval of the candidature, the general chairs are in charge of completing the organizing committee; the program chairs are in charge of selecting the topic chairs; and the topic chairs identify the rest of the technical program committee members.
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Who approves the conference budgets?

The budget is approved by VLSI-SOC steering committee and IFIP WG10.5

Who establishes registration fees?

General chairs (Tiziana Margaria and Ian O'Connor) and VLSI-SOC steering committee

Technical Program

What is the structure of the technical program committee?
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2 Program Co-Chairs, 2 Special Session Co-Chairs, 2 Publication Co-Chairs, 5 Publicity Co-Chairs, 2 Tutorial Co-Chairs, 2 Ph.D. Forum Co-Chairs, 3 industrial chairs, 2 topic Co-Chairs per track, tens of TPC members
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What materials are being reviewed?

Full Paper

What type of review is being performed?
--

Other

Other reviews being performed

Blind

What is the estimated number of submitted papers?
--

130

What is your targeted acceptance rate for submitted papers?
--

30

How many named reviewers will be associated with this conference?
--

100

How many reviewers will review each paper?

4

What criteria will be used by your reviewers to evaluate submissions?
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1.Originality and Innovation, 2. Technical Quality, 3. Significance and Impact, 4. Relevance to the Conference Topics 5. writing style
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Are you using student reviewers?
No

How will you ensure that all accepted papers will be presented?
At least one author of each accepted paper must complete a full conference registration (2 months before the conference) and present the work at the event for it to be included in the final proceedings.

Call For Papers Website URL:
www.vlsi-soc.com (not yet active for 2026 event)
Abstract Submission Date:
2026-04-21
Full Paper Submission Date:
2026-04-28
Notification of Acceptance Date:
2026-06-09
Final Paper Submission Date:
2026-07-01

Conference Contacts

Contact

Title:	Given Name/First Name:	Middle Name:	Last/Family/Surname:
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Zip/Postal Code:		

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Conference Chair			
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36 Av. Guy de Collongue, 69130 Écully, France			
Address 2:			
Address 3:			
Address 4:			
Country:	State/Province:	City:	
FR		Lyon	
Zip/Postal Code:			

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Zip/Postal Code:		
4021		

Files

Comments

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